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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Application Number	09/438,856
Filing Date	November 12, 1999
First Named Inventor	Lawrence G. Meares
Art Unit	2123
Examiner Name	Eduardo Garcia-Otero
Attorney Docket Number	3104P002

OTHER ART - NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No.'	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T?
EG	Y	DILL, H., BRATTON, K., SPARR, C., AND PITZEN, L., Application of Analog & Mixed Signal Simulation Techniques to the Synthesis and Sequencing of Diagnostic Tests.	
EG	AG	HAMIDA, NB., SAAB, K., MARCHE, D., KAMINSKA, B., AND QUESNEL, G., LIMSoft: Automated Tool for Design and Test Integration of Analog Circuits, 2nd IEEE International Test Conference, pp 571-580, 1996, Canada	
EG	AH	SLAMANI, M., KAMINSKA, B., Analog Circuit Fault Diagnosis Based on Sensitivity Computation and Functional Testing, IEEE Design and Test of Computers, pp 30-39, March 1992	

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